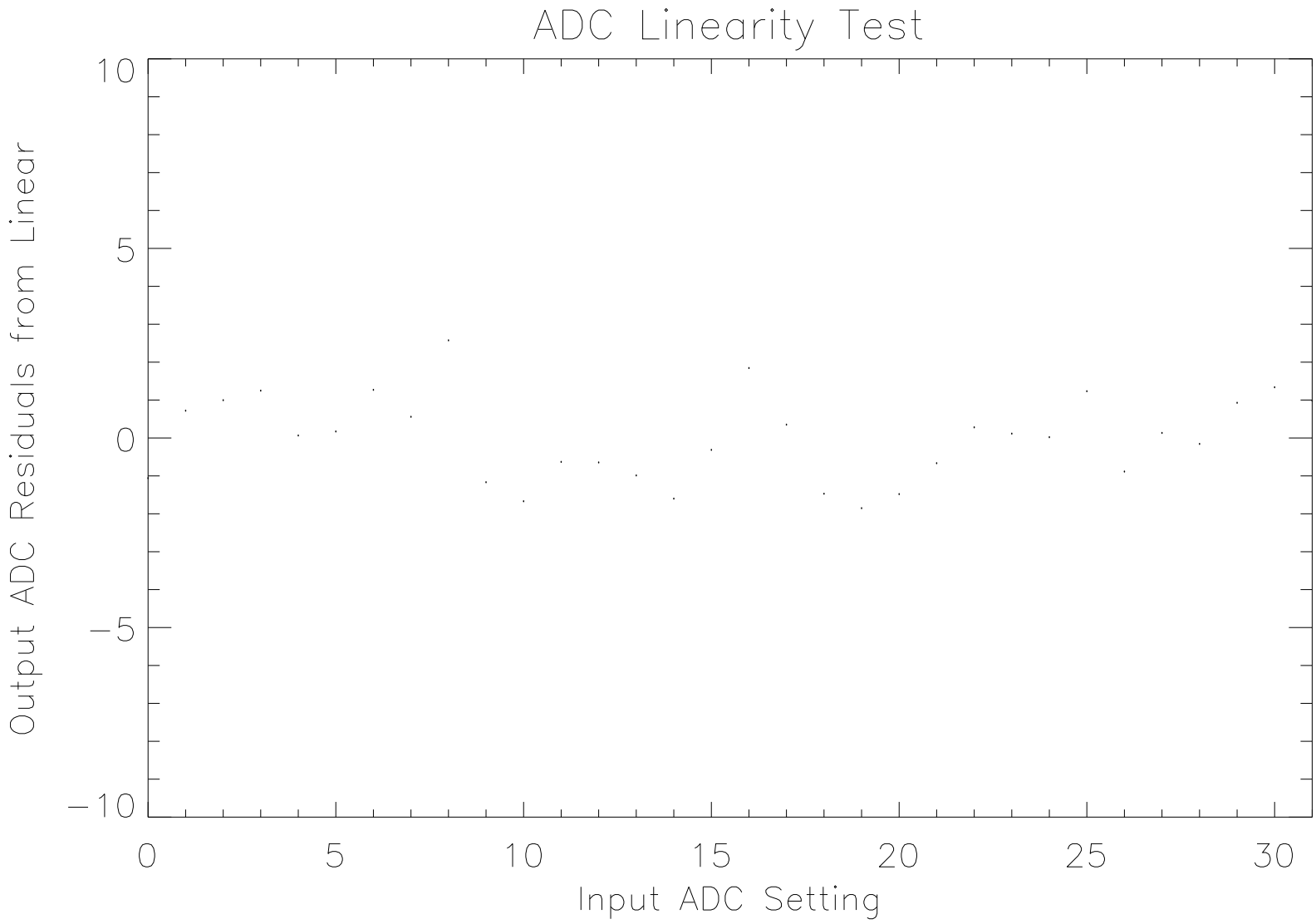
ADC Linearity Test
Output ADC Residuals from Linear
Input ADC Setting
10
5
0
−5
−10
0
5
10
15
20
25
30